



The diagram illustrates the system architecture, showing the flow of information between various components. At the top, a 'RESEARCHER' block is connected to a 'RESEARCHER' block (containing a 'RESEARCHER' block) and a 'RESEARCHER' block (containing a 'RESEARCHER' block'). The central 'SYSTEM' block is connected to a 'RESEARCHER' block (containing a 'RESEARCHER' block) and a 'RESEARCHER' block (containing a 'RESEARCHER' block'). The diagram also shows a 'RESEARCHER' block (containing a 'RESEARCHER' block) and a 'RESEARCHER' block (containing a 'RESEARCHER' block').

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[illegible]